SYIT SEM-4 Subject:-E.S (Embedded System)

	1. Which of the following offers external chips for memory and peripheral interface circuits?
	a) Microcontroller
	b) Microprocessor
	c) Peripheral system
	d) Embedded system
	2. What is CISC?
	a) Computing instruction set complex
	b) Complex instruction set computing
	c) Complimentary instruction set computing
	d) Complex instruction set complementary
	3. Sensor is?
	a) Controller
	b) Connector
	c) Changer
	d) Detector
4	. What are the functional modes of washing machine?
	a) Semi Automatic Mode b) fully-Automatic Mode
	c) Manual Mode
	d) a ,b and c
5.	Memory Mapping is?
	a) Address Mapping
	b) Data Mapping
	c) Drive Mapping
	d) System Mapping
6. W	hich of the following is necessary for the parallel input-output port?
_	ductor
	ıll-up resistor
	ash-up resistor
a) C	apacitor

7. Which Technique Processor ask question repeatedly at interval to check the device I completed the task?	has
a) Sellingb) Pollingc) Interruptd) Peripheral	
8 The time taken to respond to an interrupt is known as a) Interrupt delay b) Interrupt time c) Interrupt latency d) Interrupt function	
9.8051 microcontrollers is based on architecture? a) Liweis b) Higest c) Harvard d) Von-numan	
10. In 8051 microcontroller each of size isbit? a) 8 b) 16 c) 32 d) 64	
11.8051 microcontroller ha capacity of ? a) Uni-direction b) High-direction c) Low-direction d) Bi-direction	
12. Port-3 haspin? a) 8 b) 16 c) 32 d) 64	
13. Which is applied to a manufactured system?a) Bit patternb) Parity patternc) Test patternd) Byte pattern	

14. The unsigned char is an bit data type?
a) 4 b) 8 c) 16 d) 32
15. During selecting the controller performance is measure bya) Data spaceb) Development stylec) Speed of operationd) Cost
16.8051 support how many interrupt a) 3 b) 7 c) 6 d) 8
17.8051 support how many byte of internal data memory? a) 120 b) 128 c) 140 d) 100
18. In which design activity, the loops are interchangeable?a) Compilationb) Schedulingc) High-level transformationd) hardware/software partitioning
19. For data transferring is use?a) Componentb) Registerc) Busd) Hardware partitioning
20. Keyword is thewords? a) Reserved b) Serial c) Parallel d) temporary
21. Which variable having the global access?

- a) Local variableb) Global variablec) Permanent variable
- d) Temporary variable
- 22. Which of the following becomes a limiting factor while an application program has to be complete?
- a) Memory
- b) Peripheral
- c) Input
- d) Output
- 23. RTOS stand for ____?
- a) Real time operating system
- b) Reverse time operating system
- c) Read time operating system
- d) Reference time operating system
- 24. How a sporadic task is can turn into a periodic task?
- a) Scheduling algorithm
- b) Sporadic task event
- c) Sporadic register
- d) Sporadic task server
- 25. Which of the following is more difficult than the scheduling independent task?
- a) Scheduling algorithm
- b) Scheduling independent task
- c) Scheduling dependent task
- d) Periodic scheduling algorithm